

FIG. 1A

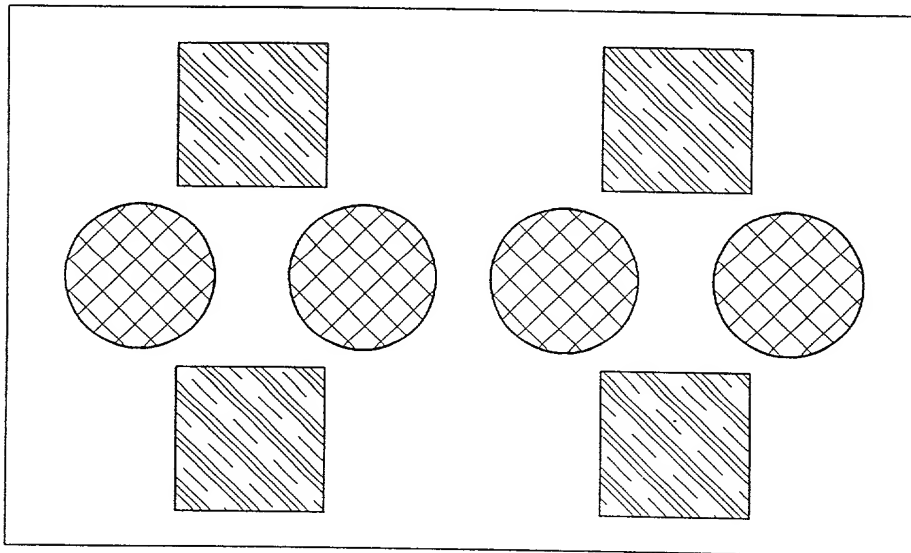


FIG. 1B

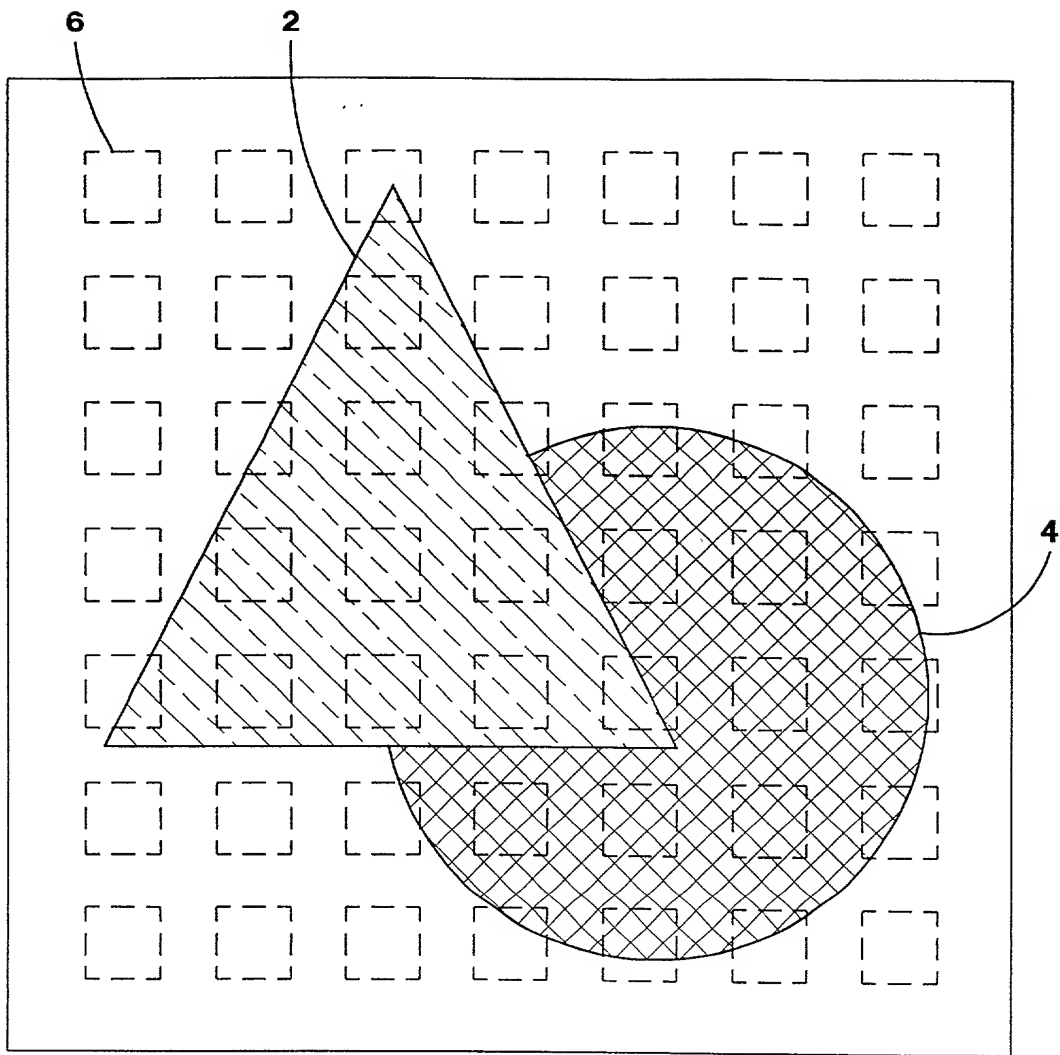


FIG. 2

FIG. 3 is a schematic diagram of a system for processing data. The system includes a data source 10, a data processor 12, and a data storage 14. The data source 10 is connected to the data processor 12, which is connected to the data storage 14. The data processor 12 is also connected to a data output 16. The data source 10 is a database, and the data processor 12 is a computer. The data storage 14 is a hard drive, and the data output 16 is a printer. The system is used for processing data from the data source 10 and storing the processed data in the data storage 14. The data output 16 is used to print the processed data.

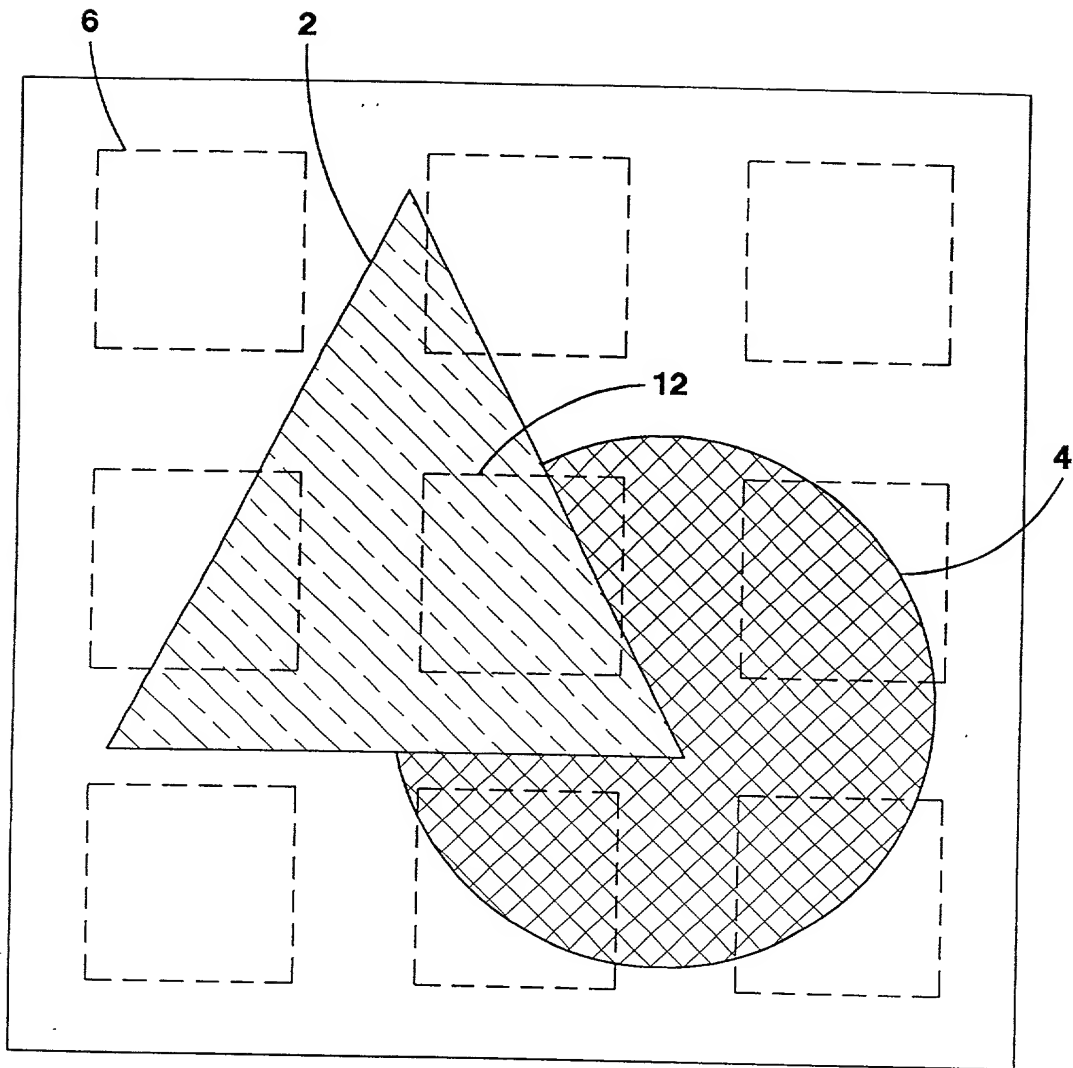


FIG. 3

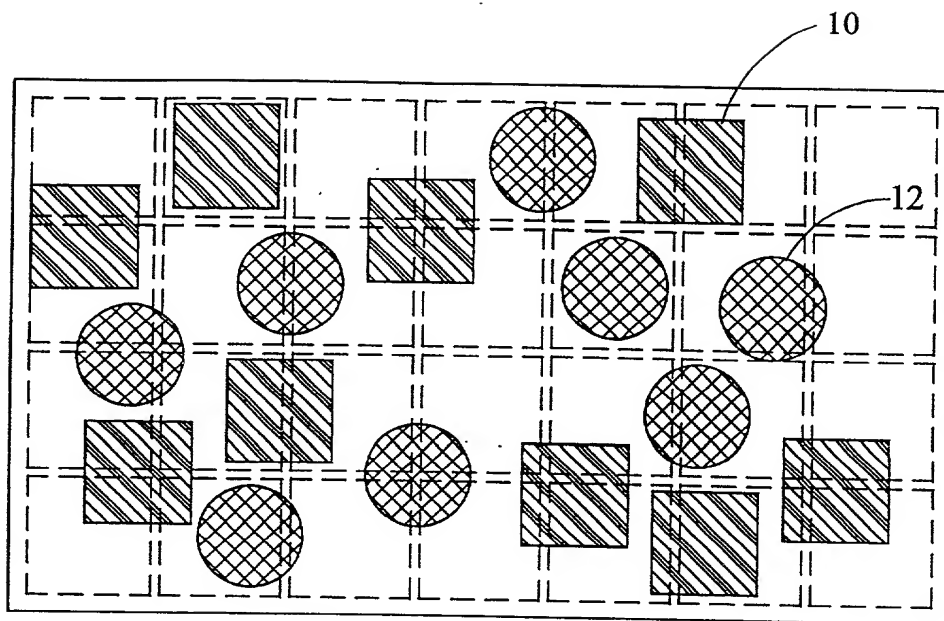


FIG. 4

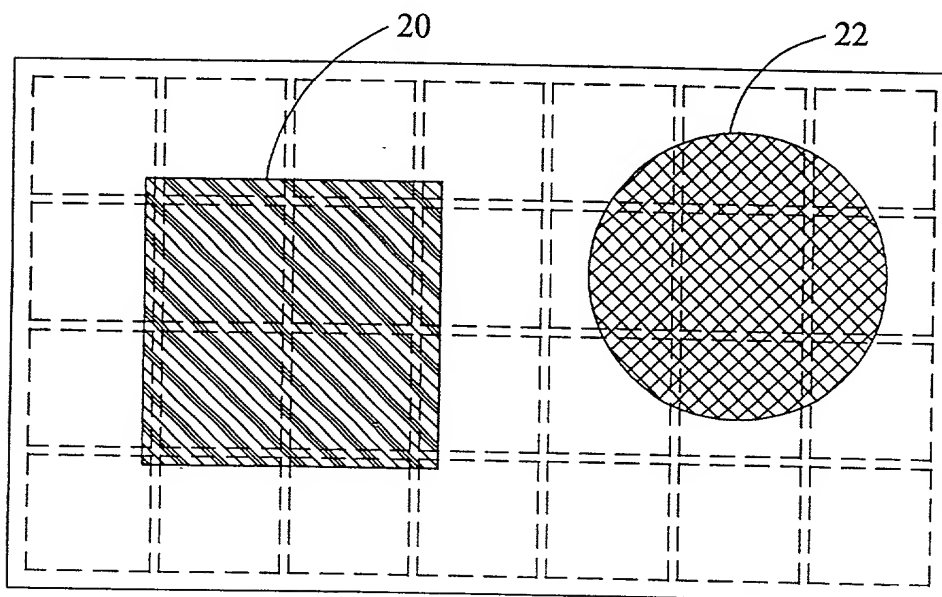
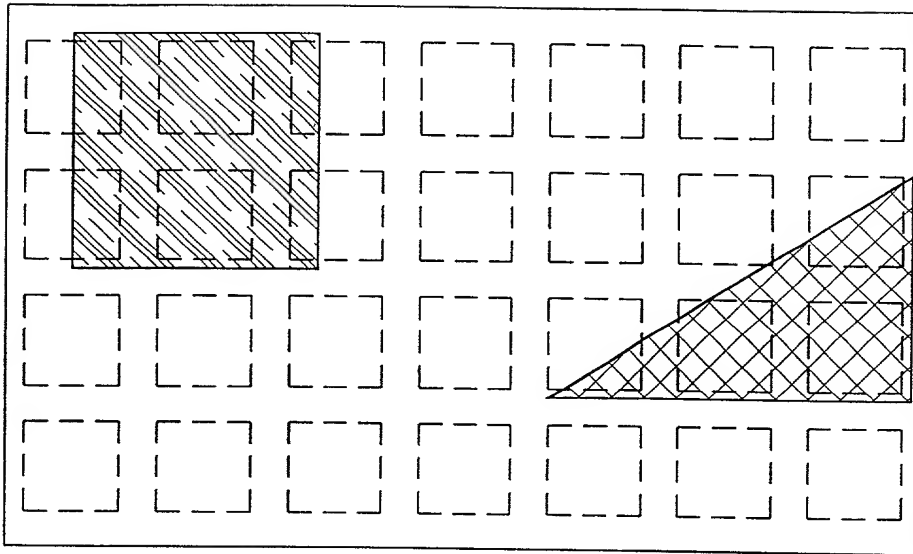
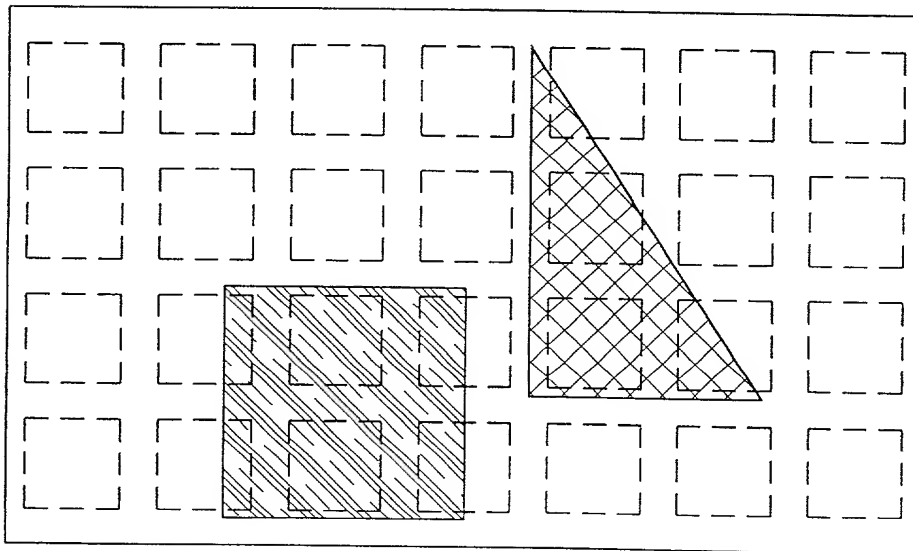


FIG. 5



**FIG. 6A**



**FIG. 6B**

	S = X1	S = X2	S = X3
$\mu_0$	0	0	0
$\mu_1$	2	0	2
$\mu_2$	1	4	2
$\mu_3$	6	3	0
$\mu_4$	5	0	0
$\mu_5$	6	2	1
$\mu_6$	3	0	0
$\mu_7$	2	3	2
$\mu_8$	0	1	0
$\mu_9$	1	0	1
$\mu_{10}$	0	0	0

FIG. 7

	0% TO 10%	10% TO 20%	20% TO 30%	30% TO 40%	40% TO 50%	50% TO 60%	60% TO 70%	70% TO 80%	80% TO 90%	90% TO 100%
$\mu_0$	0	0	0	1	0	0	0	0	0	0
$\mu_1$	1	0	0	0	0	4	0	0	1	2
$\mu_2$	0	4	0	3	0	0	3	5	1	1
$\mu_3$	0	0	3	2	0	7	0	4	1	6
$\mu_4$	0	2	0	0	4	0	0	0	0	5
$\mu_5$	0	0	0	0	0	9	0	3	0	6
$\mu_6$	0	0	5	0	0	0	0	0	0	3
$\mu_7$	0	0	0	0	6	3	0	0	0	2
$\mu_8$	0	4	0	0	1	0	0	0	2	0
$\mu_9$	0	0	0	0	0	0	0	0	0	1
$\mu_{10}$	2	0	1	0	5	0	2	0	4	0

FIG. 8

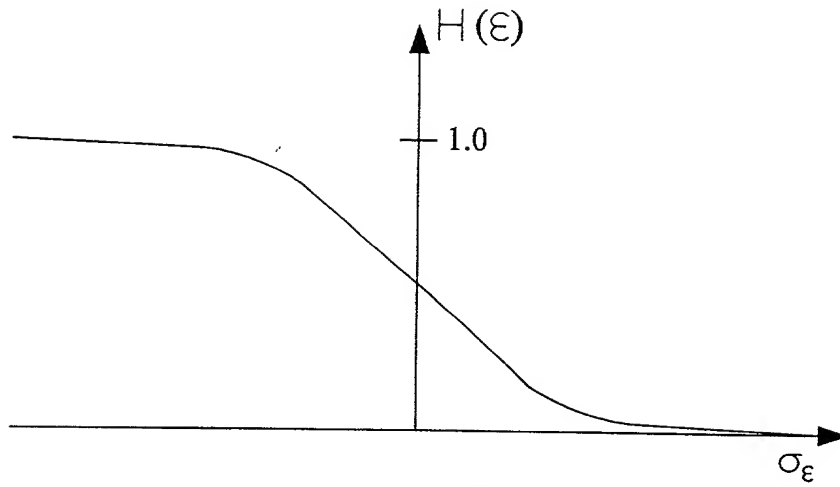


FIG. 9

	0% TO 4%	4% TO 12%	12% TO 26%	26% TO 52%	52% TO 100%
$\mu_0$	0.3	0.1	0.6	1.8	3.0
$\mu_1$	1.5	0.3	0.7	0.9	0.0
$\mu_2$	1.9	4.3	0.0	3.1	2.1
$\mu_3$	0.0	0.0	3.9	2.1	1.7
$\mu_4$	4.5	2.2	0.3	0.0	4.0
$\mu_5$	0.0	0.1	0.0	0.0	0.0
$\mu_6$	9.1	0.0	5.3	0.0	4.3
$\mu_7$	0.0	10.2	9.3	6.7	6.1
$\mu_8$	0.0	4.7	0.0	0.0	1.2
$\mu_9$	0.0	0.0	0.3	0.1	0.0
$\mu_{10}$	2.2	3.2	1.7	0.0	5.2

FIG. 10